09932361_WDS

Plus Search

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ability 1
able 1
abstract 1
accon 1
accordingly 1
accurate 1
actions 1
activities 1
activity 3
actual 1
addition 1
adjusted 1
advantages 1
after 1
allocation 1
amount 1
an 1
and 12
any 1
apparatus 1
appliances 1
application 8
applications 3
approaches 1
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area 1
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battery 3
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behavior 1
brief 1
build 2
but 1
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can 1
cases 1
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causing 1 cellular 1 certain 1 change 1 changed 1 changes 3 channels 2 chauvel 1 circuit 8 circuitry 1 circuits 3 claims 1 clock 1 co 1 compatibility 1 complex 2 compute 1 computed 1 computers 1 concurrent 1 conditions 1 conjunction 1 constraints 2 consumption 4 continuously 1 controlled 1 controlling 1 convenience 1 coprocessors 2 cost 2 count 1 counters 1 current 2 currently 1 darvin 1 dedicated 1 description 1 design 1 designed 1 designing 1 designs 2 desirable 1 determined 1 device 4 die 1 digital 2 disabling 1 disclosure 1

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dissipate 2
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distribution 1
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domain 1
dominique 1
draw 1
dsp 1
dsps 4
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dynaimc 1
dynamic 1
dynamically 1
edwards 1
enabled 1
energy 12
ensure 2
environment 1
environmental 1
errant 1
especially 2
estimated 1
eu 1
european 1
evolve 1
exceeded 1
exceeding 1
executed 4
execution 1
factor 1
failure 1
field 2
filed 1
focus 1
for 6
forefront 1
from 3
fully 1
functionality 2
further 1
future 1
general 1
gerard 1
global 1
handheld 1
handle 1
has 4
have 2
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heat 2 hence 1 herein 1 hereinafter 1 high 1 idle 1 if 2 impairing 1 important 3 importantly 1 improve 1 in 22 includes 1 including 3 inconvenient 1 incorporated 1 increase 1 information 4 integrated 6 internet 1 intervals 1 invention 5 inverno 1 involve 2 is 8 issue 1 it 4 less 1 levels 1 life 1 likely 1 list 1 loaded 1 low 3 maintaining 1 management 5 managing 1 many 4 maximize 1 maximum 1 may 5 measurements 1 meet 1 method 1 microprocessor 1 mimmizrng 1 minimum 1 modules 4

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monitored 1
monitoring 1
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moved 1
mpus 3
multimedia 1
multiple 4
multiprocessing 1
multiprocessor 4
necessary 2
network 1
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not 2
nt 4
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obtain 1
october 1
of 20
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one 1
operate 1
operation 2
or 5
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overheating 1
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packaging 1
particularly 1
patent 1
pdas 1
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priority 1 problems 1 processes 1 processing 7 processor 3 processors 3 profile 1 profiles 1 provides 2 providing 2 recharge 1 reduce 1 reducing 1 reference 1 related 1 relates 1 relatively 1 require 2 responsive 1 savings 1 scenario 1 scenarios 2 scheduling 1 selectively 1 serial 2 serious 1 seriously 1 several 1 severe 1 short 1 signal 1 significant 1 since 1 single 1 size 2 small 2 smart 1 software 1 some 2 specific 2 speed 2 speeds 1 static 1 subsystems 1 such 5 summary 1 support 1 system 5

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thresholds 1
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uncertain 1
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unodate 1
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use 2
used 2
using 2
various 5
where 3
which 3
while 1
will 9
wireless 1
with 5
within 1
without 2
years 1
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09932361_LIST PLUS Search Results for S/N 09932361, Searched May 20, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated sear ch

system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that a re

most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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6138229
6105101
5933647
6021438
6046742
6327594
5860139
5440740
4412281
5828881
5913052
6526462
5784616

09932361_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returne

From A Search of 09932361 on May 20, 2004

3 345/505 (1 OR, 2 XR) Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR INTERFACE PROCESSING, AND SELECTIVE VISUAL DISPLAY SYSTEMS COMPUTER GRAPHIC PROCESSING SYSTEM 345/501	4 AGEM	718/105 Class ENT OR TASK 718/100 718/102 718/105	718	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: VIRTUAL MACHINE TASK OR PROCESS MAN MANAGEMENT/CONTROL TASK MANAGEMENT OR CONTROL .Process schedulingLoad balancing
SYSTEMS COMPUTER GRAPHIC PROCESSING SYSTEM 345/502 .Plural graphics processors 345/505 .Parallel processors (e.g., identical processors) 3 712/35 (2 OR, 1 XR) Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS TRUCTION PROCESSING 712/1 PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program control 712/34Including coprocessor 712/35Digital Signal processor 3 712/37 (1 OR, 2 XR) Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS TRUCTION PROCESSING 712/1 PROCESSING ARCHITECTURES AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS TRUCTION PROCESSING 712/1 PROCESSING ARCHITECTURE .Microprocessor or multichip or multimodule processor having sequential program control 1 712/37Programmable (e.g., EPROM)		Class		: COMPUTER GRAPHICS PROCESSING, OPERATOR
TRUCTION PROCESSING PROCESSING ARCHITECTURES AND INS PROCESSING ARCHITECTURE PROCESSING ARCHITECTURE PROCESSING ARCHITECTURE PROCESSING ARCHITECTURE PROCESSING ARCHITECTURE PROCESSING ARCHITECTURE Including coprocessor Including coprocesso	DIS	345/501 345/502		COMPUTER GRAPHIC PROCESSING SYSTEM .Plural graphics processorsParallel processors (e.g., identical
PROCESSING PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program contr 712/34Including coprocessor 712/35Digital Signal processor 3 712/37 (1 OR, 2 XR) Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS TRUCTION PROCESSING PROCESSING 712/1 PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program contro 1Programmable (e.g., EPROM)	3	•		: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
712/1 PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program contr 712/34Including coprocessor 712/35Digital Signal processorDigital Signal processor 3 712/37 (1 OR, 2 XR) 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS TRUCTION PROCESSING PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program contro 1Programmable (e.g., EPROM)	TRUC'	rion		
712/34Digital Signal processor 3 712/37 (1 OR, 2 XR) Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS TRUCTION PROCESSING PROCESSING ARCHITECTURE 712/32 PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program contro 1Programmable (e.g., EPROM)	o.l			PROCESSING ARCHITECTURE .Microprocessor or multichip or multimodule
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712/1 PROCESSING ARCHITECTURE 712/32 .Microprocessor or multichip or multimodule processor having sequential program contro 1 712/37Programmable (e.g., EPROM)	TRUC	rion		
712/37Programmable (e.g., EPROM)	,			PROCESSING ARCHITECTURE .Microprocessor or multichip or multimodule
3 718/103 (0 OR, 3 XR)	T	712/37		Programmable (e.g., EPROM)
	3	718/103	(0	OR, 3 XR)

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AGEM	ENT O	Class R TASK	718	09932361_CLSTITLES : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: VIRTUAL MACHINE TASK OR PROCESS MAN
		718/100 718/102 718/103		MANAGEMENT/CONTROL TASK MANAGEMENT OR CONTROL .Process schedulingPriority scheduling
	345/			OR, 2 XR) : COMPUTER GRAPHICS PROCESSING, OPERATOR INTERFACE PROCESSING, AND SELECTIVE VISUAL
510		345/501 345/502		
				OR, 2 XR) : COMPUTER GRAPHICS PROCESSING, OPERATOR INTERFACE PROCESSING, AND SELECTIVE VISUAL
DIS	PLAY			SYSTEMS
		345/501 345/502 345/504		
2	345/			OR, 1 XR) : COMPUTER GRAPHICS PROCESSING, OPERATOR INTERFACE PROCESSING, AND SELECTIVE VISUAL
DISPLAY				
		345/700		SYSTEMS OPERATOR INTERFACE (E.G., GRAPHICAL USER INTERFACE)
		345/764 345/853		On-screen workspace or objectHierarchy or network structure
2	345/			OR, 2 XR) : COMPUTER GRAPHICS PROCESSING, OPERATOR INTERFACE PROCESSING, AND SELECTIVE VISUAL
DISPLAY				
		345/961		SYSTEMS OPERATOR INTERFACE WITH VISUAL STRUCTURE OR FUNCTION DICTATED BY INTENDED USE
		345/964		.CAD or CAM (e.g., interactive design tools)
2	702/	1	40	OD 2 VD)
2	103/			OR, 2 XR) : DATA PROCESSING: STRUCTURAL DESIGN, MODELING, SIMULATION, AND EMULATION
		703/1		STRUCTURAL DESIGN

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2	709/201 Class		
RDIN	ATING 709/200 709/201		MULTICOMPUTER DATA TRANSFERRING .Distributed data processing
2			OR, 0 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO
KD1N.	ATING 709/200 709/223 709/224		MULTICOMPUTER DATA TRANSFERRING .Computer network managingComputer network monitoring
2			OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/100 710/107		
2	710/242 Class 710/240	(1 710	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT ACCESS ARBITRATING
2	710/242 710/48 Class 710/1 710/36 710/48	(0 710	.Decentralized arbitrating OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Input/Output access regulation Input/Output interrupting
2	710/74 Class 710/1 710/62 710/72 710/74	-	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Peripheral adapting Application-specific peripheral adapting For data storage device
2 TRUC	712/34 Class TION	(0 712	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS

	712/1 712/32	09932361_CLSTITLES PROCESSING PROCESSING ARCHITECTURE .Microprocessor or multichip or multimodule processor having sequential program contro	
1	712/34	Including coprocessor	
2	712/36 Class	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS	
TRUC	CTION		
7	712/1 712/32 _.	PROCESSING PROCESSING ARCHITECTURE .Microprocessor or multichip or multimodule processor having sequential program contro	
1	712/36	Application specific	
2	713/300 Class		
	713/300	COMPUTER POWER CONTROL	
2	713/322 Class	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT	
	713/300 713/320 713/322	COMPUTER POWER CONTROL .Power conservationBy clock speed control (e.g., clock on/off)	
2	713/324 Class		
	713/300 713/320 713/324	COMPUTER POWER CONTROL .Power conservationBy shutdown of only part of system	
2	717/178 Class	OR, 1 XR) : DATA PROCESSING: SOFTWARE DEVELOPMENT, INSTALLATION, AND MANAGEMENT	
		SOFTWARE INSTALLATION .NetworkIncluding distribution of softwareIncluding downloading	
2	718/104 Class	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING	

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SYSTEMS: VIRTUAL MACHINE TASK OR PROCESS MAN

AGEMENT OR TASK

MANAGEMENT/CONTROL

TASK MANAGEMENT OR CONTROL 718/100

.Process scheduling 718/102 718/104

.. Resource allocation

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5237686 43 5437042 43 5481727 43 5758172 43 6081901 43 6124866 43 6230279 43 5291614 43 5511200 43 5724534 43 5802382 43 5848295 43 6092179 43	6292830 5442789 5790817 5517668 5748468 5638530 6115131 5594860 5600844 4937781 5249265 5404515 5444848 5959596 6047127 5459814 6144865 5708816 6088659 6089512 5492125 5983010 6078290 4885694	52 51 50 50 50 50 50 49 49 49 49 49 44 44 44 44 44 44 43
	5758172 6081901 6124866 6230279 5291614 5511200 5724534 5802382 5848295	43 43 43 43 43 43 43 43

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Most Frequently Occurring Classifications of Patents Returned From A Search of 09932361 on May 20, 2004

Original Classifications

- 2 709/224
- 2 712/35
- 2 713/300
- 2 718/105

Cross-Reference Classifications

- 3 718/103
- 2 345/502
- 2 345/504
- 2 345/505
- 2 345/964
- 2 703/1
- 2 709/201
- 2 710/107
- 2 710/48
- 2 710/74
- 2 712/34
- 2 712/37
- 2 713/322
- 2 718/105

Combined Classifications

- 4 718/105
- 3 345/505
- 3 712/35
- 3 712/37
- 3 718/103
- 2 345/502
- 2 345/504
- 2 345/853
- 2 345/964
- 2 703/1
- 2 709/201
- 2 709/224
- 2 710/107
- 2 710/242
- 2 710/48
- 2 710/74
- 2 712/34
- 2 712/36
- 2 713/300
- 2 713/322
- 2 713/324

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2 717/178 2 718/104

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